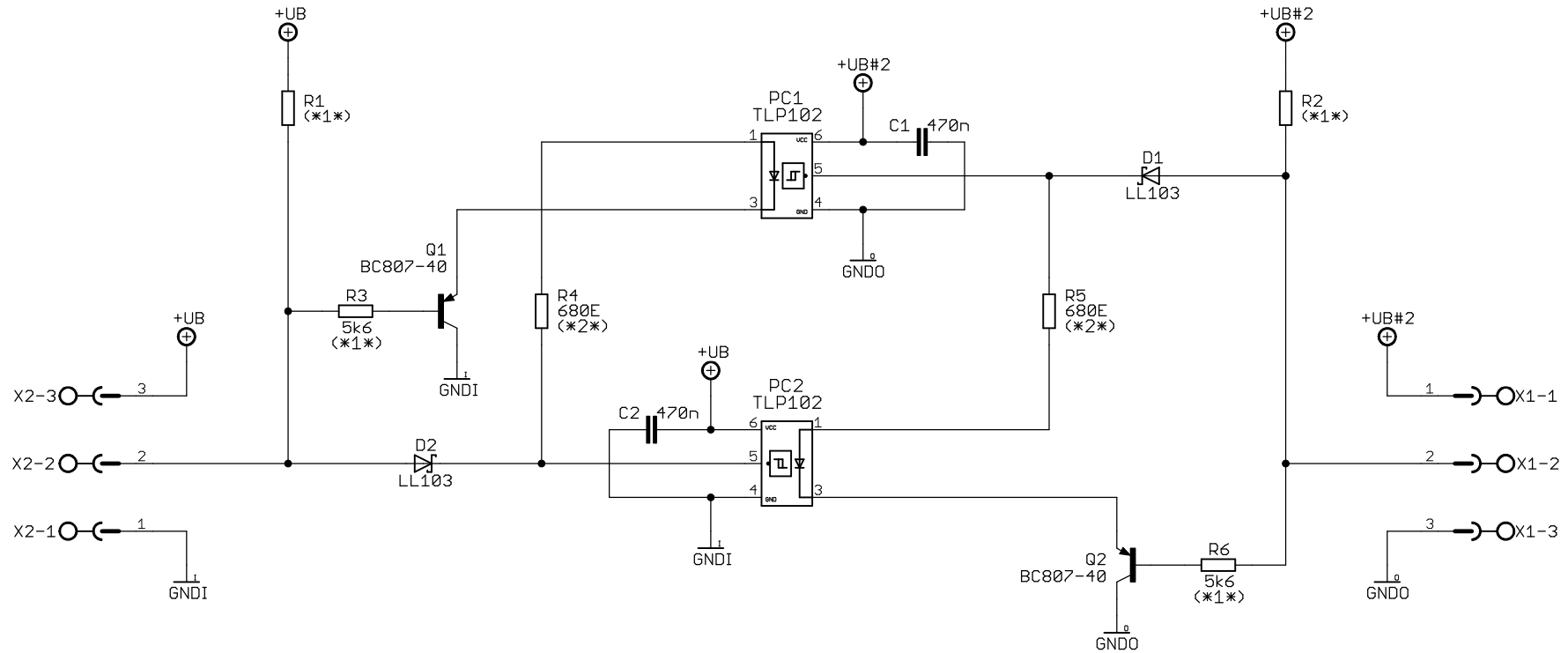


Another basic I2C-isolation circuit proposal.  
An update to the 2002 V2.0 variant

- no filters/bypass/EMC/tuning... shown.
- only one signal drawn.



(\*1\*) depending on presence of other pulls (up/down)

(\*2\*) depending on voltage (680E: 5.0V+/-10% ;-)

# IThreeC

Inter-Integrated-Isolated Circuit

(c) ASkr  
www.askrprojects.net

TITLE: IThreeC\_V31

Document Number:

REV:  
V3.1

Date: nicht gespeichert!

Sheet: 1/1